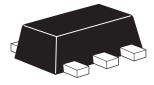
### 100V N-CHANNEL ENHANCEMENT MODE MOSFET

#### **SUMMARY**

 $V_{(BR)DSS}$ =100 $V: R_{DS(on)}$ =0.7 $\Omega$ ;  $I_D$ =1.4A

#### **DESCRIPTION**

This new generation of Trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage power management applications.



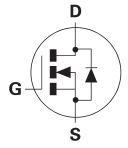
#### **SOT89**

#### **FEATURES**

- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate drive
- SOT89 package

### **APPLICATIONS**

- DC-DC Converters
- Power Management functions
- Disconnect switches
- Motor control



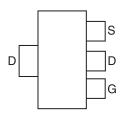
#### **ORDERING INFORMATION**

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL	
ZXMN10A07ZTA	7"	12mm	1000 units	

# DEVICE MARKING

• 7N10

### **PINOUT**



(Top view)



### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current @ V <sub>GS</sub> =10V; T <sub>A</sub> =25°C (b)	I <sub>D</sub>	1.4	А
@ V <sub>GS</sub> =10V; T <sub>A</sub> =70°C <sup>(b)</sup>		1.1	
@ V <sub>GS</sub> =10V; T <sub>A</sub> =25°C <sup>(a)</sup>		1.0	
Pulsed Drain Current <sup>(c)</sup>	I <sub>DM</sub>	4.2	А
Continuous Source Current (Body Diode) (b)	Is	2.1	А
Pulsed Source Current (Body Diode) (c)	I <sub>SM</sub>	4.2	Α
Power Dissipation at T <sub>A</sub> =25°C <sup>(a)</sup>	P <sub>D</sub>	1.5	W
Linear Derating Factor		12	mW/°C
Power Dissipation at T <sub>A</sub> =25°C <sup>(b)</sup>	P <sub>D</sub>	2.6	W
Linear Derating Factor		21	mW/°C
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55 to +150	°C

### THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient <sup>(a)</sup>	$R_{\Theta JA}$	83.3	°C/W
Junction to Ambient <sup>(b)</sup>	$R_{\Theta JA}$	47.4	°C/W

#### NOTES

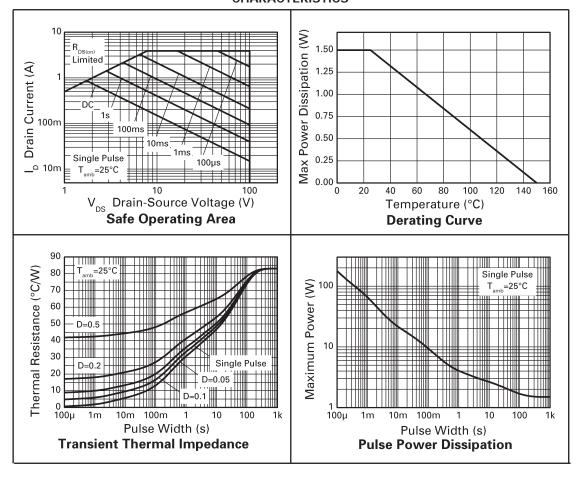


<sup>(</sup>a) (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

<sup>(</sup>b) For a device surface mounted on FR4 PCB measured at t  $\leq$  10 sec.

<sup>(</sup>c) Repetitive rating 25mm x 25mm FRA PCB, D = 0.02, pulse width 300μs - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

#### **CHARACTERISTICS**





# **ELECTRICAL CHARACTERISTICS** (at $T_{amb} = 25$ °C unless otherwise stated).

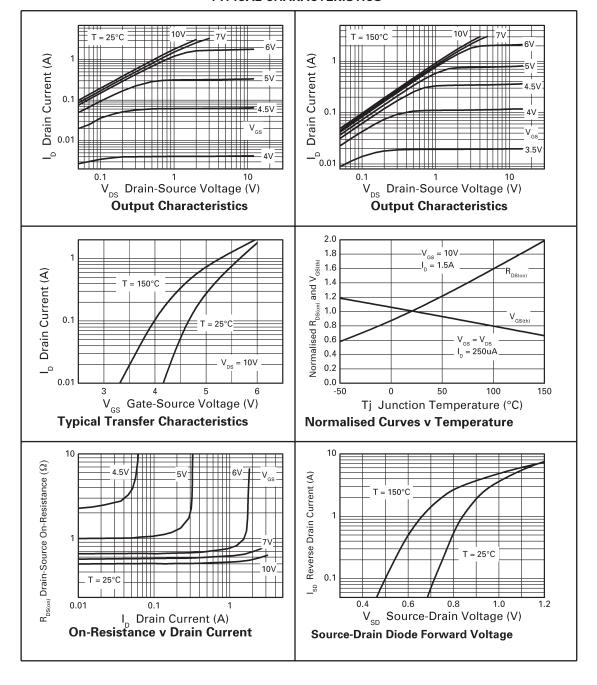
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	100			V	I <sub>D</sub> = 250μA, V <sub>GS</sub> =0V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μΑ	V <sub>DS</sub> = 100V, V <sub>GS</sub> =0V	
Gate-Body Leakage	I <sub>GSS</sub>			100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	2.0			V	I <sub>D</sub> = 250μA, V <sub>DS</sub> =V <sub>GS</sub>	
Static Drain-Source On-State	R <sub>DS(on)</sub>			0.7	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.5A	
Resistance <sup>(1)</sup>				0.9	Ω	V <sub>GS</sub> = 6V, I <sub>D</sub> = 1A	
Forward Transconductance (1) (3)	g <sub>fs</sub>		1.6		S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 1A	
DYNAMIC (3)	•	•	•	•	•		
Input Capacitance	C <sub>iss</sub>		138		pF	\/ F0\/ \/ 0\/	
Output Capacitance	C <sub>oss</sub>		12		pF	V <sub>DS</sub> = 50V, V <sub>GS</sub> =0V f=1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		6		pF	71= 11VI     Z	
SWITCHING <sup>(2) (3)</sup>			•	•	•		
Turn-On-Delay Time	t <sub>d(on)</sub>		1.8		ns		
Rise Time	t <sub>r</sub>		1.5		ns	V <sub>DD</sub> = 50V, I <sub>D</sub> = 1A	
Turn-Off Delay Time	t <sub>d(off)</sub>		4.1		ns	$R_{G} \approx 6.0 \Omega$ , $V_{GS} = 10 V$	
Fall Time	t <sub>f</sub>		2.1		ns		
Total Gate Charge	Qg		2.9		nC		
Gate-Source Charge	Q <sub>gs</sub>		0.7		nC	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 10V	
Gate Drain Charge	Q <sub>gd</sub>		1		nC	I <sub>D</sub> = 1A	
SOURCE-DRAIN DIODE							
Diode Forward Voltage <sup>(1)</sup>	V <sub>SD</sub>		0.85	0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> = 1.5A, V <sub>GS</sub> =0V	
Reverse Recovery Time <sup>(3)</sup>	t <sub>rr</sub>		27		ns	T <sub>j</sub> =25°C, I <sub>S</sub> = 1A,	
Reverse Recovery Charge <sup>(3)</sup>	Q <sub>rr</sub>		12		nC	di/dt=100A/μs	

#### NOTES

- (1) Measured under pulsed conditions. Pulse width  $\leq 300 \mu s;$  duty cycle  $\leq 2\%.$
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.

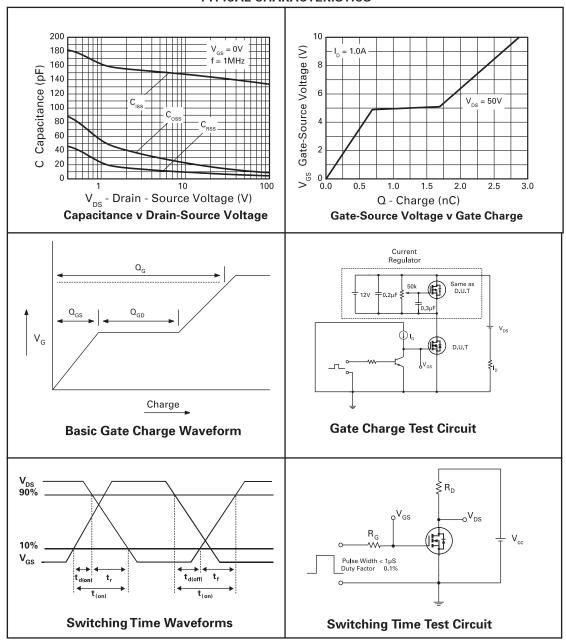


#### TYPICAL CHARACTERISTICS



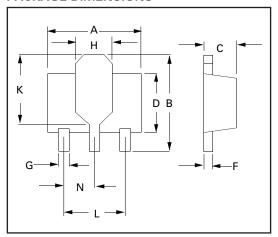


### TYPICAL CHARACTERISTICS

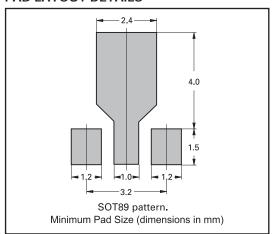




#### **PACKAGE DIMENSIONS**



#### PAD LAYOUT DETAILS



DIM	Millimetres		Inches		
DIIVI	Min	Max	Min	Max	
Α	4.40	4.60	0.173	0.181	
В	3.75	4.25	.150	0.167	
С	1.40	1.60	0.550	0.630	
D	-	2.60	-	0.102	
F	0.28	0.45	0.011	0.018	
G	0.38	0.55	0.015	0.022	
Н	1.50	1.80	0.060	0.072	
K	2.60	2.85	0.102	0.112	
L	2.90	3.10	0.114	0.112	
N	1.4	1.60	0.055	0.063	

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